



D9485 DAVIC QPSK Bridge Release Note

This release note provides release information for D9485 1.2.21 release software.

Release Details

This following table lists component version numbers and other details for this release.

Release Package Name:	Rel_4P_1_2_21-G
Overlay File System:	1.1.98
Ramdisk:	1.1.42
Kernel:	1.1.10
Bootloader:	1.1.7
Bootloader Environment:	1.1.5
Flattened Device Tree:	1.1.10
Primary Microcontroller:	0.9.3
3AN Boot FPGA:	1.0.7
Spartan6 Demodulator FPGA:	0.2.28
Virtex5 Primary FPGA:	1.34.3
Chopstick Modulator UPX:	MV_CHP_80000003_20121019-00112320.0007

Document Version:

This is the first draft release of this document.

Site Requirements

This section includes information on site requirements for running D9485 software release 1.2.21. Please read this entire section before you begin an upgrade.

System Release Compatibility and Prerequisites

This software can be installed on a network that is running *System Release 4.2 Service Pack 4* and later.

For a complete configuration listing, or to upgrade your system, contact Cisco Services for assistance. Follow the menu options to speak with a service engineer.

Hardware Requirements

The 1.2.21 release works with all D9485 DAVIC QPSK Bridges that are presently deployed.

QPSK Software Installation

Refer to the *Model D9485 DAVIC QPSK Bridges Installation and Operation Guide* (part number OL-30211) for instructions on installing the QPSK software.

Fixed Defects

Number	Summary
CSCuv79866	Fixed an issue where the Modulator Power Level was being reset to the default value of 60 dBmV during reboots instead of being saved.

Known Issues

No known issues exist in this release

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